

REMARKS

1. Rejection under 35 U.S.C. 102

All of the claims were rejected under 35 U.S.C. 102 over U.S. Patent 6,496,067 to Behzad et al. The current application was filed by Behzad to cover a new design that includes a resistive ladder for providing the bias for each transconductance stage. The Official Action refers to biasing circuit 16 of the '067 patent. Biasing circuit 16 does show a circuit that provides a plurality of biasing signals, but the circuit 16 is not specifically shown to be a biasing circuit that comprises a resistive ladder. While a biasing ladder could be used as a biasing circuit 16, the '067 patent simply does not state that biasing circuit 16 is a ladder and therefore does not anticipate the claims of the present application (to the same named primary inventor). Each of the independent claims is amended and requires such a resistive ladder. As such, it is believed that each of the claims overcomes the rejection under Section 102.

2. Rejection under Double Patenting Doctrine

Claims 1-3, 4, 7-9, 12 and 13 had been rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claims 1-3, 6, 4-6, 9-10, and 16, respectively, of US Patent No. 6,496,067. A terminal disclaimer is tendered herewith to overcome this rejection.

As a result of the foregoing, the Applicant respectfully submits that the remaining claims in the Application are in condition for allowance, and respectfully requests an early allowance of such Claims.

If any issues arise, or if the Examiner has any suggestions for expediting allowance of this Application, the Applicant respectfully invites the Examiner to contact the undersigned at the telephone number indicated below or at jharrison@texaspatents.com.

Appl. No. 10/769,178
Amendment dated May 12, 2006
Reply to Office Action mailed Jan. 12, 2006

Docket No. BP 2475

The Commissioner is hereby authorized to charge any additional fees connected with this communication or credit any overpayment to Garlick Harrison & Markison Deposit Account No. 50-2126 (ref. BP2457).

Respectfully submitted,

Date: May 12, 2006

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